EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	9	(US-7362061-$ or US-6437611-$ or US-6236239-$ or US-5214320-$ or US-5081380-$ or US-4797579-$ or US-6885225-$ or US-6084378-$ or US-5994885-$).did.	USPAT	OR	ON	2008/06/04 12:23
L2	4	1 and (pwm modulat$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/06/04 12:23
L3	9	1 and (reduc$4 decreas$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/06/04 12:30
L4	4	1 and (reduc$4 decreas$4) and (pwm modulat$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/06/04 12:31
L5	2	1 and (reduc$4 decreas$4) and (pwm modulat$4) and (position angle angular)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/06/04 12:53
S1	268	318/811 and ((limit$4 reduc$4 decreas$4) near4 voltage) and (gate near4 (transistor igbt mosfet fet))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/05/31 22:20
S2	265	318/811 and ((limit$4 reduc$4 decreas$4) near4 voltage) and (gate near4 (transistor mosfet fet))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/05/31 22:21

S3	136	318/811 and ((limit$4 reduc$4 decreas$4) near4 voltage) and (gate near4 (transistor igbt mosfet fet)) and (driv$4 near4 (mosfet fet transistor))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/05/31 22:22
S4	74	318/811 and ((limit$4 reduc$4 decreas$4) near4 voltage) and (gate near4 (transistor igbt mosfet fet)) and (driv$4 near4 (mosfet fet transistor)) and (gate near4 control$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/05/31 22:22
S5	54	318/811 and ((limit$4 reduc$4 decreas$4) near4 voltage) and (gate near4 (transistor igbt mosfet fet)) and (driv$4 near4 (mosfet fet transistor)) and (gate near4 control$4) and invert$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/05/31 22:24
S6	14	("4797579" | "5081380" | "5214320" | "5514981" | "5557223" | "6008686" | "6043698" | "6236239" | "6359797" | "6437611" | "6469585" | "6476654" | "6507226" | "6714049").PN. OR ("7362061").	US-PGPUB; USPAT; USOCR	OR	ON	2008/05/31 22:25

		URPN.				
S7	68	318/811 and ((limit$4 reduc$4 decreas$4) near4 voltage) and (gate near4 (transistor igbt mosfet fet)) and (driv$4 near4 (mosfet fet transistor)) and (gate near4 (driv $4 control$4)) and invert$4 and (pwm modulat$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/05/31 22:29

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